

Tohoku University
71th Nano-Spin Engineering Workshop

—Terahertz Graphene Devices—

Date&Time: 14:00—17:00, March 15th, 2014 (Thu.)

Venue: Room A508, Nano-Spin Laboratory Building, 5F
RIEC, Tohoku University, Katahira Campus
2-1-1 Katahira, Aoba-ku, Sendai, 980-8577, Japan

Language: English

Program:

5/15
14:00-15:30 **“Introduction to numerical methods for the solution of Maxwell’s equations“ (in English)**
Dr. Igor SEMENIKHIN, RIEC Visiting Associate Professor
(Senior Researcher, Institute of Physics and Technology, RAS, Moscow, Russia)

Over the past decades an increasing interest has been devoted to the rigorous solution of Maxwell’s equations in order to study the optical properties of optoelectronic devices including image sensors, nanostructured solar cells, photonic crystals and diffraction gratings. Such structures exhibit geometrical dimension comparable or even smaller than the radiation wavelengths, therefore the solution of Maxwell’s equations is required. To date a large number of algorithms have been developed to solve Maxwell’s equations for the analysis of these structures. Among them, the algorithms based on eigenmode expansion are widely used. The presentation will focus special attention on the Fourier modal method (FMM) also known as Rigorous Coupled-Wave Analysis method (RCWA). Some other modal methods including Polynomial expansion modal method and Analytic modal method will also be discussed.

15:30-17:00 **“Tunneling in semiconductors and graphene” (in English)**
Dr. Dmitry SVINTSOV, JSPS Postdoctoral Fellow
(Researcher, Institute of Physics and Technology, RAS, Moscow, Russia)

Tunneling in semiconductors much differs from free electron tunneling and is quite similar to Schwinger’s mechanism of electron-positron vacuum breakdown. Despite such pure science analogy, this phenomenon finds its applications in common devices – lasers, microwave generators, and flash memories. Absence of band gap in graphene allows almost unimpeded electron tunneling between valence and conduction bands. This effect (called sometimes ‘Klein paradox’) is actually a killer of all logic device applications of graphene. Recent advances in layered heterostructures composed of graphene, boron nitride, and transition metal chalcogenides start a new age in the evolution of graphene-based tunneling devices.

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