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附属ナノ・スピン実験施設
研究報告書 第9号**

**Research Report No.9
Laboratory for Nanoelectronics and Spintronics
Research Institute of Electrical Communication
Tohoku University**

2014

施設研究報告書 2014

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Annual Research Report 2014

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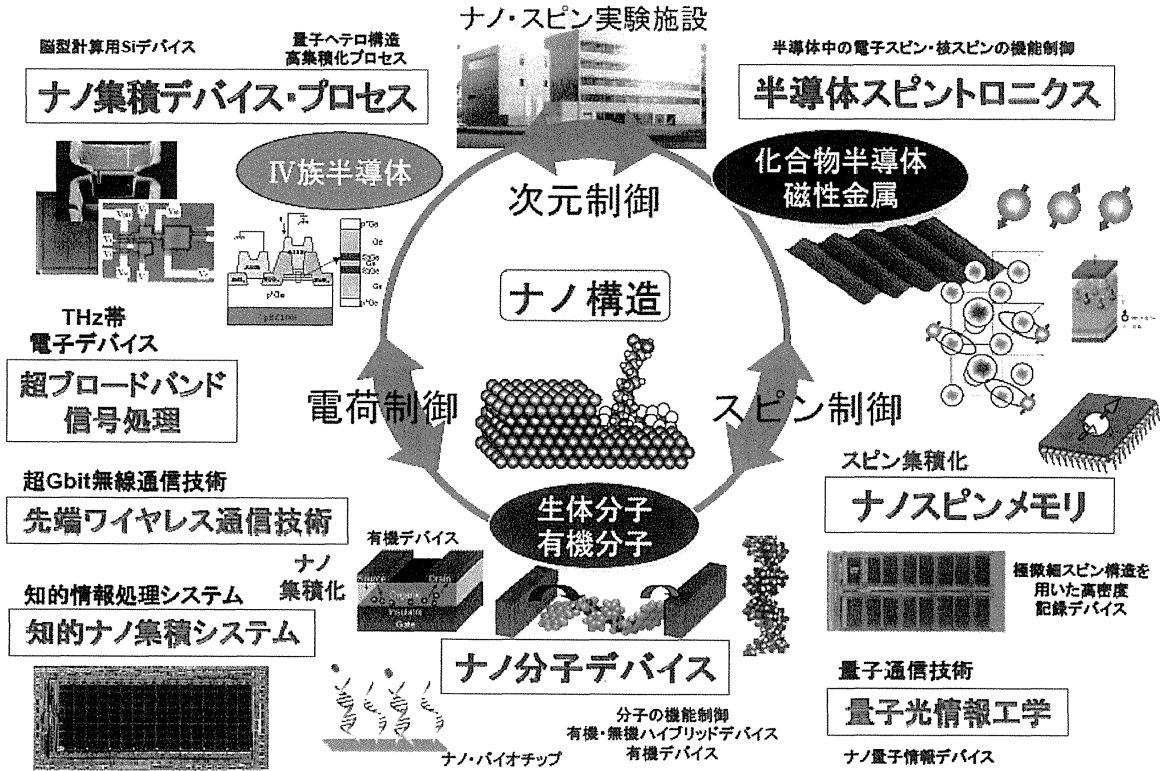
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1. 施設の概要

Outline

ナノ・スピントロニクス実験施設

～情報通信を支えるナノ・スピントロニクス基盤技術の創生～



「ナノ・スピントロニクス実験施設」は、本研究所附属研究施設として平成16年4月1日に設置された。その目的は、情報通信を支えるナノエレクトロニクス・スピントロニクス基盤技術の創生することにある。これを実現するため、「ITプログラムにおける研究開発推進のための環境整備」によって整備されたナノ・スピントロニクス総合研究棟とその主要設備を用いて、本研究所および本所と密接な関係にある本学電気・情報系の各研究分野と共にナノテクノロジーに基づいた電子の電荷・スピントロニクスを駆使する基盤的材料デバイス技術の研究開発を進め、さらに全国・世界の電気通信分野の研究者の英知を結集した共同プロジェクト研究を推進する。

現在、ナノ・スピントロニクス総合研究棟では、「ナノ・スピントロニクス実験施設」が推進するナノヘテロプロセス、半導体スピントロニクス、ナノ分子デバイスの各基盤技術を担当する施設研究室と施設共通部、及び知的ナノ集積システム研究室、量子光情報工学研究室、超ブロードバンド信号処理研究室が入居し連携して研究を進めている。これらの陣容で、上記基盤技術を創生し、ナノエレクトロニクス・スピントロニクスにおける世界のCOEとなることを目標としている。

東北大学電気通信研究所附属
ナノ・スピントロニクス実験施設長
教授 庭野 道夫

2. 施設の組織

Organization

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教授 庭野 道夫 Michio Niwano

共通部 Technical Office
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非常勤研究員 西村 容太郎 Youtaro Nishimura



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教授 中島 康治 Koji Nakajima
教授 枝松 圭一 Keiichi Edamatsu
教授 尾辻 泰一 Taiichi Otsuji

3. 平成 25 年度の研究成果のハイライト

Highlights of Research in FY2013

施設研究部と利用研究室の平成 25 年度の研究成果のハイライトを記します。

ナノ集積基盤技術関連

Nano Integration

● ナノ集積デバイス・プロセス (佐藤茂雄・櫻庭政夫)

Nano-Integration Devices and Processing (S. Sato and M. Sakuraba)

(1) 大規模神経回路の構築を目的として、自動的に動作マージンを最大化する機構を有する多数決回路をベースに、大きなファンインを有するニューロン回路の設計を行った。離散値シナプスを組み込んだ場合の動作を調べ、ニューロン回路として所望の動作が得られることを確認した。また、最適化問題を効率的に解くことができる自励機能と高次結合を持つ神経回路網を FPGA 上に実装し、その性能を確認した。特に、実装できるニューロン数が FPGA 上のメモリ量によって制限されることを回避するために、動的な荷重値生成が有効であることを示した。

(2) 基板非加熱 ECR プラズマ CVD 装置による Si(100)上への歪 SiGe 混晶及び歪 Ge 薄膜のエピタキシャル成長とその格子歪の変化について実験研究を進め、Si(100)上への SiGe 混晶 ($0 < \text{Ge 比率} < 1$) 薄膜形成における SiH_4 と GeH_4 の反応速度定数の比率は、Ge 比率によらずにほぼ同程度であるが、SiGe 混晶薄膜の Ge 比率の増加とともに増加することを見いだした。また、Si(100)上に堆積された Ge 比率 0.50 の SiGe 混晶薄膜では、膜厚 11 nm までは Si(100)基板に格子整合し、面内圧縮歪を維持してエピタキシャル成長させることに成功した。

(1) Toward the huge integration of neural networks, we have designed a neuron circuit having large fan-in ability by modifying a majority circuit, which has self-adjusting function to maximize operating margin, and its successful operation has been confirmed together with discrete synapses. We have also implemented a self-oscillating neural network with higher order synapses using an FPGA, and confirmed that dynamic synaptic weight calculation is effective for saving memories.

(2) Epitaxial growth of strained $\text{Si}_{1-x}\text{Ge}_x$ alloy and Ge films on Si(100) using low-energy ECR plasma CVD without substrate heating has been studied. It is found that reaction rate ratio for SiH_4 and GeH_4 is scarcely dependent on Ge fraction in the films, while the reaction rates tend to increase with increase of the Ge fraction. Moreover, epitaxial growth of highly strained 11 nm-thick $\text{Si}_{0.50}\text{Ge}_{0.50}$ alloy film lattice-matched to Si(100) substrate has been realized without substrate heating.

● 知的ナノ集積システム (中島康治)

Intelligent Nano-Integration System (K.Nakajima)

(1) 高次シナプス結合を持つ逆関数遅延ニューロンモデルを FPGA 上に実装し大規模化の方向を検討した。その結果を災害時断絶ネットワーク対応の移動ノードスケジューリングへ適用しその有効性の検証を推し進めた。さらに最適化問題や連想記憶などの高速数値解析を実現する逆関数ゼロ遅延モデルを提案して高次結合との組み合わせや DS-Net との組み合わせにより、その有効性を TSP-LIB 中の問題に適用して確認するとともに、QAP へも適用しこの問題に対しても 100%の正解率を達成した。(2) 逆関数ゼロ遅延モデルを連想記憶の相互想起動作へ適用し、大きな記憶容量と広いベースサイズを実現した。(3) 8×8 ビット並列乗算器の一部を集積回路として製作し測定を行った。また、超伝導量子干渉デバイスによるニューロ素子を利用したホップフィールド形ネットワークを構成し、集積回路によるニューロン回路の基礎特性の実測を行い、動作を確認し

た。

(1) By using high-order synapses for an inverse function delayed neural network, we set up an FPGA circuit for traveling salesman and quadratic assignment problems. Furthermore, we proposed an inverse function delay-less model for high speed numerical calculation of artificial neural networks, and we applied the model to some problems in TSP-LIB. (2) We applied ID networks to development of a hetero-associative memory system. In that system we obtained large memory capacity and wide basin size. (3) We demonstrated successfully a 4-bit parallel multiplier using a carry look-ahead adder with niobium integrated circuits to improve the performance of high-speed operation for the SFQ fast Fourier transform, and we fabricated the central part of an 8-bit parallel multiplier of SFQ system. A neuron circuit using superconducting quantum interference devices was fabricated and successfully demonstrated.

半導体スピントロニクス基盤技術関連

Semiconductor Spintronics and information technology

● 半導体スピントロニクス・ナノスピンメモリ (大野英男・池田正二)

Semiconductor Spintronics and Nano-Spin Memory (H. Ohno and S. Ikeda)

固体中のスピンと電荷の自由度を使ったスピントロニクスの基盤技術の確立とその工学的応用を目的として研究を行い、以下の成果を得た。(1) (Ga,Mn)As/p-GaAs 積層構造において、マイクロ波によるスピンポンピング及び p-GaAs へのスピン流の注入を実現し、特性を起電力測定により詳細に解明した。(2) 垂直磁化容易 CoFeB-MgO 磁気トンネル接合(CoFeB/MgO p-MTJ)において、低消費電力な p-MTJ の書き込み方法として期待される電界磁化反転の磁化反転時間をサブナノ秒に短縮した。(3) 垂直磁化容易 Ta/CoFeB/MgO の積層構造において、面内電流誘起トルクにより CoFeB 層に作用する有効磁界を求める手法を確立した。(4) CuIr チャンネル上に作製した面内磁化容易 CoFeB-MgO MTJ において、面内電流誘起トルクを利用した MTJ のスイッチングに成功した。(5) Co/Ni 細線において、電流による磁壁デビニング確率のパルス幅、及び電流密度依存性を解明した。(6) GaAs/AlGaAs 単一量子井戸において、スピンのコヒーレント状態をゲート電界により制御することに成功し、スピン輸送において有用な知見を得た。(7) 2重 CoFeB-MgO 界面記録層と積層フェリ参照層を開発し、直径 11 nm の磁気トンネル接合(MTJ)での動作実証に成功した。

・連携研究

1. 最先端研究開発支援プログラム「省エネルギー・スピントロニクス論理集積回路の研究開発」において、参画研究室と連携して以下の成果を得た。(1) 6T-2MTJ セルを用いた 1Mb の STT-MRAM を試作し、1.5ns/2.1ns の読み出し/書き込みをランダムに行えることを実証し、L3 キャッシュに適用可能であることを示した。(2) 4T-2MTJ 不揮発 TCAM (ternary content-addressable memory)セル回路のパワー比較を行い、6T-SRAM (static random access memory)に対する優位性を示した。(3) 高磁気異方性、低磁気緩和を有し、20 nm 以下のサイズを有する MTJ 材料として有望な FePd 及び MnAl を用いた MTJ の実証に成功した。

2. 文部科学省「次世代 IT 基盤構築のための研究開発」の委託研究である「耐災害性に優れた安心・安全社会のためのスピントロニクス材料・デバイス基盤記述開発」プロジェクトにおいて、参画研究室と連携して以下の成果を得た。(1) p-MTJ チップへの放射線入射の影響をシミュレーションにより解明した。

Our research activities focus on the establishment of fundamental technologies for future spintronics devices. The outcomes in the last fiscal year are following. (1) Demonstration of spin-pumping by microwave and observation of inverse spin hall voltage in (Ga,Mn)As/p-GaAs (2) Demonstration of

electric field-induced magnetization switching in a CoFeB/MgO based magnetic tunnel junction with a perpendicular magnetic easy axis (p-MTJ) with switching time less than 1 ns (3) Establishment of the method to determine the current-induced effective fields with Ta/CoFeB/MgO stack (4) Demonstration of the magnetization switching with current-induced effective field in CoFeB/MgO based MTJ fabricated on the CuIr channel (5) Elucidation of the pulse duration and current density dependences of the current-induced domain wall depinning in Co/Ni fine wire (6) Demonstration of the gate voltage control of the spin coherence in GaAs/AlGaAs quantum well (7) Development and demonstration of a CoFeB based p-MTJ with 11 nm in diameter by using a double interface MgO/CoFeB/Ta/CoFeB/MgO recording layer with synthetic ferrimagnetic reference layer.

1. Research activities in "Research and development of ultra-low power spintronics-based VLSIs" under granted by JSPS through the FIRST program. (1) A 1Mb STT-RAM with a 6T2MTJ cell is designed and fabricated using 90nm CMOS/MTJ process that can operate in 1.5nsec/2.1nsec random read/write cycle. The RAM is fast enough to be applicable to embedded memories such as L3 cache. (2) Demonstration of the superiority of 4T-2MTJ nonvolatile ternary content-addressable memory (TCAM) cell compared to the conventional 6T-SRAM (static random access memory) in the operation power. (3) Development of FePd and MnAl electrodes, which are promising candidates for 20-nm-diameter MTJ with high thermal stability and low operating power.

2. Research activities in "Research and Development of Spintronics Material and Device Science and Technology for a Disaster-Resistant Safe and Secure Society" program under Research and Development for Next- Generation Information Technology of MEXT. (1) Elucidation of the irradiation effect in p-MTJs with simulation.

● 超ブロードバンド信号処理（尾辻泰一・末光哲也・ポーバンガトンベットステファン）

Ultra-Broadband Signal Processing (T.Otsuji, T.Suemitsu, and S. Boubanga Tombet)

いまだ未開拓な電磁波領域であるミリ波・テラヘルツ波帯の技術を開拓し、次世代の情報通信・計測システムへ応用することを目的として、新しい集積型のミリ波・テラヘルツ波電子デバイスの創出と、それらを応用した超ブロードバンド信号処理技術に関する研究開発を推進している。本年度は、単原子層炭素材料：グラフェンを利得媒質とする新原理テラヘルツレーザーを提案し、自然放出ならびに誘導放出の室温観測に成功するとともに、表面プラズモンポラリトンによる巨大利得増強作用を理論的に発見し、実験実証にも成功した。また、プラズモン共鳴を原理とする超高感度・低雑音テラヘルツ波検出デバイスの開発に成功し、室温動作では自らの世界最高感度記録を更新（200 GHz、1.5 THz 入射時に各々22.7 kV/W、6.4 kV/W の検出感度）した。

The goal of our research is to explore the terahertz frequency range by creating novel electron devices and systems. Graphene, a monolayer sheet of honeycomb carbon crystal, exhibits unique carrier transport properties owing to the massless and gapless energy spectra, which is expected to break through the limit on conventional device operating speed/frequency performances. First, we theoretically discovered and experimentally verified the giant THz gain of the surface plasmon polaritons in population-inverted graphene. We also experimentally verified the spontaneous and stimulated THz emission in optically pumped graphene at room temperature. Second, we developed plasmon-resonant THz emitters/detectors, succeeding in world-first coherent monochromatic THz emission and breaking the record sensitivity of 22.7 (6.4) kV/W at 220-GHz (1.5-THz) radiation.

● 量子光情報工学（枝松圭一・小坂英男・三森康義）

Quantum-Optical Information Technology (K. Edamatsu, H. Kosaka, and Y. Mitsumori)

1. 多光子量子もつれ光子対の発生・制御・検出方法の開発

量子測定における、ある物理量の測定誤差と他の物理量の擾乱との間の不確定性関係は、量子論の本質的性質であるのみならず、量子計測、量子通信への応用上も重要な意味をもつ。本研究

室では、光子の偏光の一般化測定における誤差・擾乱の不確定性関係を検証する実験研究を行い、ハイゼンベルクの誤差・擾乱の関係式が破れ、近年新たに最提唱された関係式（小澤の不等式およびブランシアードの不等式）が成立していることを明らかにした（Scientific Reports, Phys. Rev. Lett. 誌に掲載，報道発表）。

2. 量子中継のための量子メディア変換デバイスの開発

量子情報通信における通信距離を飛躍的に増大するための量子中継器の実現を目指し、情報伝送を担う光子がもつ量子情報を情報処理を担う電子スピンへと転写する量子メディア変換インターフェース技術の開発を進めている。本年度は、(1)光子から電子スピンへの状態転写および検出過程を量子トモグラフィ測定するにより転写および検出過程が量子的であることを示し、その忠実度を明らかにした（Phys. Rev. B 誌に発表）、(2) ダイヤモンド中の単一窒素空孔欠陥を用いた電子スピン量子ビットへの光量子状態転写、検出の基礎実験、等の成果を得た。

3. 半導体量子ドットの超高速コヒーレント制御

半導体量子ドット中の電子状態のラビ振動によるコヒーレント制御法の実現は量子情報処理を目指す固体デバイスを実現する上で必要不可欠な技術開発である。本年度は、フォトンエコー法を用いて励起子ラビ振動を観測し、量子ドットの新しい光学効果である局所電場効果を観測した（Phys. Rev. B 誌に発表）。

1. We have experimentally tested the error-disturbance uncertainty relations by weak measurement of photon polarization and demonstrated that Heisenberg relation is violated while Ozawa and Branciard relations hold.

2. We are developing a quantum media converter from a photon to an electron spin to realize a quantum repeater, which is expected to extend the transmission distance of quantum info-communication. We have demonstrated that (1) the process of state transfer and readout is quantum like by the measurement of electron spin state tomography, and (2) fundamental experiments for the photonic state transfer to an electron spin memory and electron spin state readout with a photon in diamond.

3. We have investigated the excitonic Rabi oscillations in semiconductor quantum dots using photon echo spectroscopy for the development of the optical coherent control of the electric states in the quantum dots. We have observed the interesting behavior of the Rabi oscillations arising from the local field effect in the quantum dots.

ナノ分子デバイス基盤技術関連

Nano-Molecular Devices

● ナノ分子デバイス（庭野道夫・木村康男）

Nano-Molecular Devices (M. Niwano and Y. Kimura)

1. 赤外分光法を用いた脂肪細胞の分化過程のその場観察法の実現

多重内部反射型赤外分光法を用いて、脂肪細胞の分化過程（脂肪滴形成過程）のその場観察に成功した[1]。現在、生活習慣病としての肥満症を抑制するための薬の開発が続けられているが、その中で簡便かつ迅速な薬物スクリーニング手法の実現が強く求められている。我々は、上記分光法を用いて脂肪滴の形成の初期段階を検知することに成功し、その成果は今後の抗肥満薬開発への応用が期待される。

2. 局所陽極酸化法による TiO₂ マイクロガスセンサの開発

Ti 金属薄膜の局所陽極酸化により、TiO₂ ナノチューブ薄膜をベースとしたマイクロスケールのガスセンサの作製に成功した[3]。さらに、TiO₂ ナノチューブ孔内壁に金属ナノ微粒子を電気化学的に担持することにより、検出感度やガス識別能を向上できることを明らかにした。

3. TiO₂ ナノチューブ薄膜をベースとした有機/無機ハイブリッド型太陽電池の開発

TiO₂ ナノチューブ薄膜を用いた有機・無機混合型太陽電池の開発を行っている。ナノスケールのチューブ孔の内壁にナノスケールの P3HT ポリマー薄膜をコーティングするための内壁表面処理法の開発に成功した[5]。

1. In-situ monitoring of adipocyte differentiation by infrared spectroscopy (IRAS)

We have succeeded in real-time monitoring cell differentiation to adipocytes by using IRAS with the multiple internal reflection (MIR) geometry. We showed that MIR-IRAS has a potential to evaluate antiadipogenic agents in terms of their effects on fat and protein synthesis during adipogenesis.

2. Development of TiO₂ nanotube-based micro-scaled gas-sensor

We have fabricated TiO₂ nanotube-based macro-scaled gas sensors by locally anodizing a thin Ti film on glass substrate. We demonstrated that deposition of metal nanoparticle on the inner wall of nanotubes improves the sensitivity and gas-discrimination function of the gas sensors.

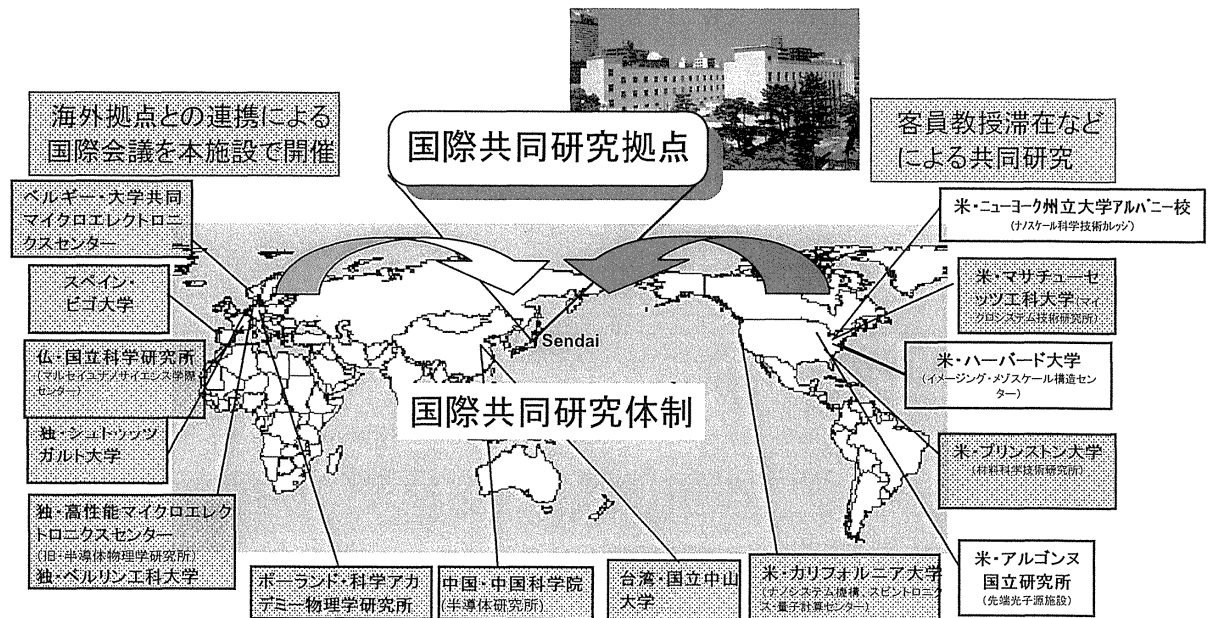
3. Development of organic/TiO₂ hybridized solar cells

We have investigated modification of TiO₂ surfaces used for fabrication of TiO₂/polymer hybrid solar cells. We found that modification of TiO₂ surfaces with [6,6]-phenyl-C61-butyric acid (PCBA) drastically increased the short circuit current of TiO₂/P3HT-based hybrid solar cells.

4. 施設の活動

4-1 ナノエレクトロニクス国際共同研究拠点の創出

平成 17 年度～21 年度特別教育研究経費として採択されたナノエレクトロニクス国際共同研究拠点創出事業を基盤として、21 世紀に求められる高度な情報通信を実現するため、「半導体立体ナノ構造の実現と応用」、「半導体中のスピン制御技術の確立と応用」、「分子ナノ構造による情報処理の実現と応用」の 3 本を柱に据え、ナノエレクトロニクス情報デバイスと、これを用いた情報システムの構築を推進するとともに、これらを実現するための国際共同研究体制を構築し、ナノエレクトロニクス分野の世界におけるセンターオブエクセレンスの確立を目指している。



大学間協定 独・ベルリン工科大学
米・ハーバード大学

部局間協定 独・高性能マイクロエレクトロニクスセンター
仏・国立科学研究所マルセイユナノサイエンス学際センター
中国・中国科学院半導体研究所
ポーランド・科学アカデミー
スペイン・ピゴ大学
台湾・国立中山大学

大学間覚書 米・ニューヨーク州立大学アルバニー校ナノスケール科学技術カレッジ
米・カリフォルニア大学 サンタ・バーバラ校

ナノ・スピンの実験施設で開催した国際シンポジウム

RIEC Symposium on Spintronics

(第1回: 2005年2月8-9日, 第2回: 2006年2月15-16日,
第3回: 2007年10月31日-11月1日, 第4回: 2008年10月9-10日,
第5回: 2009年10月22-23日, 第6回: 2010年2月5-6日,
第7回: 2011年2月3-4日, 第8回: 2012年2月2-3日,
第9回: 2012年5月31-6月2日, 第10回: 2013年1月15-16日,
第11回: 2013年1月31-2月1日)

International Workshop on Nanostructure & Nanoelectronics

(第1回: 2007年11月21-22日, 第2回: 2010年3月11-12日,
第3回: 2012年3月21-22日, 第4回: 2013年3月7-8日)

RIEC-CNSI Workshop on Nano & Nanoelectronics, Spintronics and Photonics

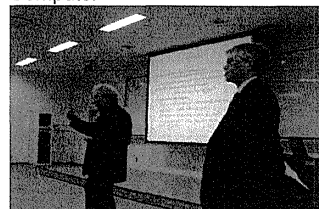
(第1回: 2009年10月22-23日)

RIEC International Symposium on Brain Functions and Brain Computer

(第1回: 2012年11月15-16日, 第2回: 2014年2月21-22日)



2nd RIEC International Symposium on Brain Functions and Brain Computer



2nd RIEC Symposium on Spintronics-MgO-based Magnetic Tunnel Junction-Left: Albert Fert (received 2007 Nobel Prize in Physics); Right: Russel Cowburn

4-2 国際シンポジウム開催 (プログラムは「6. 参考資料」に収録)

第54回電気通信研究所国際シンポジウム

第2回 脳機能と脳型計算機に関する通研国際シンポジウム The 1st RIEC International Symposium on Brain Functions and Brain Computer

佐藤 茂雄
Shigeo SATO

開催日: 平成26年2月21日(木)～22日(金)(2日間)

開催場所: 東北大学電気通信研究所 ナノ・スピン総合研究棟

本シンポジウムは、半導体工学、計算機工学、ロボット工学、数理工学、大脳生理学、神経科学、心理物理学、非線形物理学といった関連分野から広く研究者を集め、脳機能や脳型計算機に関する最近の成果・動向について、分野の垣根を超えて研究発表と議論を行うことを目的として企画・設立された。今回が二回目であり、平成26年2月21日、22日の2日間に渡って開催された。ドイツ、フランス、スウェーデンの3か国から3名の海外招待講演者を迎え、計13件の口頭発表、9件のポスター発表が行われた。講演内容は、神経科学、培養神経回路、集積回路など多岐にわたるものであったが、分野を超えて有意義な質疑応答が活発に行われた。多数の学生が参加したこともあり、若手の国際交流の機会を提供する活気あふれるシンポジウムとなった。

第 55 回電気通信研究所国際シンポジウム

第 8 回 バイオ・医療・ナノエレクトロニクスに関する国際シンポジウム、
第 5 回 ナノ構造とナノエレクトロニクスに関する国際ワークショップ

The Joint Symposium of
7th International Symposium on Medical, Bio- and Nano-Electronics,
4th International Workshop on Nanostructures & Nanoelectronics

庭野 道夫
Michio NIWANO

開催日: 平成 26 年 3 月 6 日 (木曜日) ~ 7 日 (金曜日) (2 日間)

開催場所: 東北大学電気通信研究所 ナノ・スピン実験施設

前半はナノ構造とその応用についてのセッションであり、ナノチューブやナノカーボンなどのナノ構造体について、その形成技術や、太陽電池やガスセンサなどへの多様な応用法についての発表があり、特に応用法について深い議論がなされた。また、近年注目されている酸化グラフェンの大量生成法についての発表が注目された。後半は微細加工技術のバイオ応用やメディカル応用のセッションであり、固体基板上に培養した神経細胞ネットワークの計測技術、脂質二分子膜に包埋したチャンネルタンパクを用いたセンサや、超音波を用いたイメージング技術や新規ケミカルバイオセンサについての発表があり、それらの性能や特性についての深い議論がなされた。これらの知見及び広い分野の研究者の交流は、ナノエレクトロニクスやそれらの医療応用など、今後の幅広い研究の進展に大いに貢献すると考えられる。

参加人数は、海外から 5 名の招待講演者を含め、二日間で研究者、学生など 90 名を数え、活発で有意義な討論及び情報交換が行われた。この合同シンポジウムは次年度も開催予定である。

5. 研究成果（平成 25 年度）

5 A ナノ集積基盤技術関連

Nano Integration

A1 ナノ集積デバイス・プロセス（佐藤茂雄・櫻庭政夫）
Nano-Integration Devices and Processing
(S. Sato and M. Sakuraba)

A2 知的ナノ集積システム（中島康治）
Intelligent Nano-Integration System
(K.Nakajima)

A1 ナノ集積デバイス・プロセス (佐藤茂雄・櫻庭政夫) Nano-Integration Devices and Processing (S. Sato and M. Sakuraba)

1. 脳型計算用デバイスの高密度実装技術に関する研究
High-density implementation of devices for brain computing
将来の Si-LSI の微細化限界を見据え、脳型計算機の実用化に向けて、脳型計算用デバイスの開発とその高密度実装技術、及び脳型計算機のプロトタイプについて研究を行っている。
Foreseeing the miniaturization limit of Si-LSI in future and aiming at the implementation of a practical brain computer, we study devices for brain computing, high-density implementation techniques, and a prototype of a brain computer.
2. 脳型計算用量子知能デバイスに関する研究
Intelligent quantum device for brain computing
脳型計算と量子計算を融合し究極の知能を実現するため、核スピンや超伝導体を利用した、量子ニューロン素子として働く知能デバイスとその計算アルゴリズムについて研究を行っている。
We study intelligent quantum device, which operates as quantum neuron, using nuclear spins or superconductor devices, and its computation algorithms in order to realize ultimate intelligence after the fusion of brain computing and quantum computing.
3. 高度歪 IV 族半導体エピタキシャル成長のための低損傷基板非加熱プラズマ CVD プロセスに関する研究
Low-damage plasma CVD process without substrate heating for epitaxial growth of highly strained group IV semiconductors
ナノメートルオーダー厚さの高品質量子ヘテロ構造を実現するために、原子オーダーで平坦かつ急峻なヘテロ界面を有する高度歪 IV 族半導体薄膜のヘテロエピタキシャル成長について研究している。
In order to realize nanometer-order thick high-quality heterostructure, heteroepitaxial growth of highly strained group-IV semiconductor films with atomically flat and abrupt heterointerfaces is being studied.
4. IV 族半導体高度歪量子ヘテロ構造の高集積化プロセスに関する研究
Large-scale integration process of group IV semiconductor quantum heterostructures
IV 族半導体量子効果デバイスの Si 集積回路への搭載を実現するために、IV 族半導体高度歪量子ヘテロ構造の高集積化プロセスと量子ヘテロナノデバイス製作・高性能化について研究している。
In order to integrate group-IV semiconductor quantum-effect devices into Si LSI, large-scale integration process of group-IV highly strained quantum heterostructures and fabrication of high-performance quantum hetero nanodevices are being studied.

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2. “Epitaxial Growth of Si_{1-x}Ge_x Alloy on Si(100) by ECR Ar Plasma CVD in a SiH₄-GeH₄ Gas Mixture without Substrate Heating”, N. Ueno, M. Sakuraba, J. Murota and S. Sato, 8th Int. Conf. on Si Epitaxy and Heterostructures (ICSI-8) & 6th Int. Symp. Control of Semiconductor Interfaces (ISCSI-VI), Fukuoka, Japan, June 2-7, 2013, Abs.No.P1-8.
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6. “Formation and Characterization of Strained Si_{1-x}Ge_x Films Epitaxially Grown on Si(100) by Low-Energy ECR Ar plasma CVD without Substrate Heating”, N. Ueno, M. Sakuraba, J. Murota and S. Sato, Proc. Symp. E12: ULSI Process Integration 8, San Francisco, USA, Oct. 27-Nov. 1, 2013 (ECS Trans., Vol.58, No.9, Edited by C. Claeys, H. Iwai, M. Tao, S. Deleonibus, J. Murota, The Electrochem. Soc., Pennington, NJ, 2013), pp.207-211: Abs. 224th Electrochem. Soc. Meeting, Abs.No.2228.
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9. “Neurochip using stochastic logic”, S. Sato, K. Nakajima, Abst. The 1st RIEC International Symposium on Brain Functions and Brain Computer, p. 13, Sendai, Japan, Feb. 21-22, 2014.
10. “Nano-Integration Devices and Processing”, H. Akima, M. Sakuraba, and S. Sato, Abst. The 1st International Symposium on Brainware LSI, p.10, Sendai Japan, March 28, 2014.

A2 知的ナノ集積システム（中島康治） Intelligent Nano-Integration System (K. Nakajima)

1. 集積化ニューラルネットワークの基本構成と学習性能に関する研究
Research for basic architectures of LSI neural networks and their learning efficiency
集積化ニューラルネットワークを用いた知的情報処理システムの構成法を追究し、その学習性能を評価・解析して性能向上を図る。
This research is concerned with the design of intelligent information processing systems constructed of LSI neural networks. The fabricated LSI neural networks are analyzed and evaluated to improve the learning efficiency.
2. 逆関数遅延ネットワークモデルに関する研究
Research for inverse function delayed network models
アクティブニューロンモデルである ID モデルを用いて、知的情報処理システムを目指す。
This research is concerned with the development of the intelligent processing system by using ID models which are active neuron models.
3. ニューロ的手法を利用した量子計算機に関する研究
Research for neuromorphic quantum computer
ニューロ的手法を利用した量子計算アルゴリズムの開発と、その固体素子への実装を図る。
This research is concerned both with the development of a new neuromorphic quantum computation algorithm and its implementation with solid state devices.
4. 超伝導位相モード集積回路に関する研究
Research for superconducting phase-mode LSI
磁束量子を情報担体とする超伝導集積回路で構成した新しい計算機システムを開発する。
This research is concerned with the development of new computer systems constructed of superconducting LSI circuits where single flux quanta are used as information bit carriers.

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6. K. Matsui, Y. Hayakawa, S. Sato and K. Nakajima, FPGA Implementation of the Discrete Inverse-function Delayed Neural Network with High Order Synaptic Connections, 7th International WorkShop on New Group IV Semiconductor Nanoelectronics and JSPS Core-to-Core Program Joint Seminar “Atomically Controlled Processing for Ultralarge Scale Integration,” P17, Sendai Japan, Jan. 2014.
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9. T. Onomi and K. Nakajima, Basic Technology of Integrated Systems for Artificial Neural Network, Abstract book of the 1st international symposium on Brainware LSI, p. 12, Sendai Japan, Mar. 2014.

5 B 半導体スピントロニクス基盤技術関連

Semiconductor Spintronics and Information Technology

- B1 半導体スピントロニクス、ナノスピメモリ (大野英男・池田正二)
Semiconductor Spintronics and Nano-Spin Memory
(H.Ohno and S.Ikeda)

- B2 超ブロードバンド信号処理 (尾辻泰一・末光哲也・ポーバンガトンベ
ットステファン)
Ultra-Broadband Signal Processing
(T.Otsuji, T.Suemitsu and S. Boubanga- Tombet)

- B3 量子光情報工学 (枝松圭一・小坂英男・三森康義)
Quantum-Optical Information Technology
(K.Edamatsu, H.Kosaka and Y.Mitsumori)

B1 半導体スピントロニクス、ナノスピンメモリ (大野英男・池田正二) Semiconductor Spintronics and Nano-Spin Memory (H. Ohno and S. Ikeda)

固体中の電子やスピンの状態を制御し工学的に応用するために、新しい材料の開発、量子構造の作製と性質の理解、及びそれらのスピントロニクス素子高機能素子への応用に関する研究を行っている。さらに、不揮発性により、高機能かつ低消費電力化が期待されるスピントロニクス素子、及びスピントロニクス集積回路技術の研究開発を行っている。

Our research activities cover the areas of preparation, characterization, and application of new classes of solid state materials as well as their quantum structures, in which electronic and spin states can be controlled. Furthermore, we are working on research and development of advanced technology for spintronics-based devices and integrated circuits, which are expected to realize high performance and low power consumption owing to their nonvolatility.

1. 半導体スピントロニクスに関する研究 Semiconductor Spintronics

固体中のスピンと電荷の自由度を使った省エネルギーかつ高機能なスピントロニクス素子の実現をめざして、半導体、磁性半導体、金属磁性体におけるスピン現象、及びそれらを利用した新規スピン機能材料、新規スピントロニクス素子の創生に関する研究を行っている。

We are working on spin-related phenomena in semiconductors, magnetic semiconductors, and magnetic metals as well as novel functional spin materials and devices, in order to realize low-power functional spintronic devices.

1) スピントロニクスに関する研究 Spintronics

分子線エピタキシーやスパッタリング法を用いたスピントロニクス材料や構造の作製、スピン機能物性の評価と理解。

Development of functional spin materials and structures by using molecular beam epitaxy and sputtering, understanding and characterization of spin-related phenomena are being carried out.

2) 金属磁性体とその機能素子応用に関する研究 Magnetic metal functional devices and their application

20 nm 以下のスピントロニクス素子作製および素子加工技術の開発、作製した微細スピントロニクス素子の特性評価、そしてスピントロニクス素子を利用した種々の集積回路試作を進めている。

Development of spintronic devices with the size of less than 20 nm and their processing technology, characterization of the fabricated spintronic devices, and fabrication of various prototype integrated circuits employing spintronic devices are being carried out.

3) 磁性半導体及びその量子構造の物性と応用に関する研究 Properties and application of magnetic semiconductors and their quantum structures

強磁性体と半導体を組み合わせた新しい半導体デバイスの基礎的研究を行っている。Exploration of novel spintronic semiconductor devices based on ferromagnet/semiconductor structures is being carried out.

4) 半導体量子構造における電子光スピン物性とその応用に関する研究 Characterization of electrical, optical, and spin properties of semiconductor quantum nanostructures and their applications

III-V 族化合物半導体超構造中におけるキャリアや原子核のスピンに注目し、フェムト秒オーダーの磁化分解測定を行ってそのコヒーレンスを理解するとともに、量子情報処理等への応用を研究している。

Study of ultrafast processes, especially spin dynamics of carriers in III-V semiconductor nanostructures is being carried out by femto-second time resolved measurements to the application for such as ultrafast optical switches and quantum information processing.

2. ナノスピンドバイスメモリの研究 Nano-spin device and memory

高機能低消費電力のメモリデバイスとそれによって可能となる新しい論理集積回路および情報通信処理システムを、スピン磁性を用いて実現することを目標として、スピンメモリロジック実現に向けた基盤技術を開発する。

To realize high-performance low-power consumption spin memory and logic devices, we are developing technologies to realize advanced spin memory and logic devices using magnetic tunnel junctions (MTJs) consisting of ferromagnetic metal electrodes and insulating barriers.

1) 高出力トンネル磁気抵抗素子の開発

Magnetic tunnel junctions with high output voltage

面内垂直磁気異方性トンネル磁気抵抗(TMR)素子の高出力化を行っている。

Development of high performance magnetic tunnel junctions (MTJs) consisting of ferromagnetic metal electrodes with in-plane or perpendicular magnetic easy axis and insulating barrier is being carried out.

2) 金属系スピントロニクスデバイスの開発

Metal-based spintronics devices

微細な金属系スピントロニクスデバイスの作製とその特性評価、スピンメモリロジック基本回路試作を行っている。

Fabrication of metal-based spintronic devices with small dimension and characterization of their properties and making basic spintronics-based circuits experimentally are carried out.

3) スピン注入磁化反転素子の開発

Spin transfer torque memory and logic devices

低書き込み電力に向けたスピン注入磁化反転に関する研究を行っている。

Characterizing spin transfer torque switching toward reduction of writing power is being carried out.

3. 省エネルギースピントロニクス論理集積回路の研究開発

Research and Development of Ultra-low Power Spintronics-based VLSIs

日本学術振興会「最先端研究開発支援プログラム」「省エネルギースピントロニクス論理集積回路の研究開発」において、参画研究室と連携してスピントロニクス論理集積回路基盤技術に関する研究が行われた。

Technologies based on spintronics that makes VLSIs high performance and ultra low power were studied under “Research and Development of Ultra-low Power Spintronics-based VLSIs” program granted by JSPS through the FIRST program.

1) スピントロニクス材料に関する研究

Spintronics materials

スピントロニクス論理集積回路試作用の世界最高水準のデバイス材料に関する研究が行われた。

Advanced spintronics device materials for VLSI were studied.

2) スピントロニクスデバイスに関する研究

Spintronics devices

論理集積回路用スピントロニクスデバイスの高性能化に関する研究が行われた。

Spintronics devices with low write current, high thermal stability, high TMR and high reliability were studied.

- 3) 革新的スピントロクス材料デバイスに関する研究
New Spintronics materials and devices
電界効果型磁化制御デバイス、半導体スピントロクス材料デバイス技術に関する研究が行われた。
Magnetic materials and devices for electric-field control, and semiconductor spintronic materials and devices were studied.
 - 4) スピントロクス論理集積回路に関する研究
Spintronics based logic circuits
専用ならびに汎用スピントロクス論理集積回路向けアプリケーションの設計原理検証に関する研究が行われた。
Design and verification of spintronics logic integrated circuits, and design for high-speed and stable operation of spintronics VLSI were studied.
4. 耐災害性に優れた安心安全社会のためのスピントロクス材料デバイス基盤技術に関する研究
Research and Development of Spintronics Material and Device Science and Technology for a Disaster-Resistant Safe and Secure Society
- 文部科学省「次世代 IT 基盤構築のための研究開発」の委託研究である「耐災害性に優れた安心安全社会のためのスピントロクス材料デバイス基盤記述開発」プロジェクトにおいて、プロジェクト参画研究室と連携して高機能スピントロクスワーキングメモリ向け材料デバイスの開発と大容量スピントロクスワーキングメモリ向け材料デバイスの開発が行われた。
High-speed spintronics working memory and high-density spintronics working memory were studied in “Research and Development of Spintronics Material and Device Science and Technology for a Disaster-Resistant Safe and Secure Society” program under Research and Development for Next- Generation Information Technology of MEXT.
- 1) 高速スピントロクスワーキングメモリに関する研究
Developments of high-speed spintronics working memory
磁気トンネル接合を基本構造とする高速二端子スピントロクス素子、及び高速三端子スピントロクス素子を作製し、基礎特性を調べた。
High-speed two terminal and three terminal devices based on magnetic tunnel junction were fabricated and studied.
 - 2) 大容量ワーキングメモリに関する研究
Developments of high-density spintronics working memory
40 nmφ以下の極微細なトンネル磁気接合を作製し、基礎特性を調べた。
Magnetic tunnel junctions with a diameter less than 40 nmφ were fabricated and studied.

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2. T. Ono, "Current-induced domain wall motion: spin transfer torque v.s. spin Hall torque (**invited**)," The 8th International Conference on Advanced Materials and Devices, Jeju, Korea, December 11, 2013
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B2 超ブロードバンド信号処理（尾辻泰一・末光哲也・ポーバンガトンベツトステファン）

Ultra-Broadband Signal Processing

(T.Otsuji, T.Suemitsu and S. Boubanga-Tombet)

新原理ミリ波・テラヘルツ波帯集積電子デバイスの研究

Novel millimeter-wave and terahertz-wave integrated microelectronic devices

いまだ未踏の電磁波領域であるミリ波・テラヘルツ波（サブミリ波）帯の技術を開拓、実用化するために、本領域で動作する新しい電子デバイスおよび回路システムの創出と、それらの情報通信・計測システムへの応用に関する研究開発を行っている。第一に、半導体ヘテロ接合構造に発現する二次元プラズモン共鳴という新しい動作原理に立脚した集積型のコヒーレントテラヘルツ電磁波発生・信号処理デバイスの研究開発を進めている。電子デバイス・光子デバイス双方の動作限界を同時に克服するブレイクスルーとして注目している。第二に、サブ波長領域に局在した低次元プラズモンの分散特性を光電子的に制御することによって、高次の信号処理機能を果たす新たなテラヘルツ帯メタ材料・回路システムの創出に取り組んでいる。第三に、新材料：グラフェン（単層グラファイト）を用いた新原理テラヘルツレーザーならびに極限高速トランジスタの開発を推進している。さらに、これら世界最先端の超ブロードバンドデバイス・回路を応用して、超高速無線通信や安心・安全のための新たな計測技術の開発を進めている。

We are developing novel, integrated electron devices and circuit systems operating in the millimeter-wave and terahertz regions. One example is the frequency-tunable plasmon-resonant terahertz emitters, detectors, and modulators. Another example is unique electromagnetic metamaterial circuit systems based on optoelectronic dispersion control of low-dimensional plasmons. We are also pursuing graphene-based new materials to create new types of terahertz lasers and ultrafast transistors, breaking through the limit on conventional transistor/laser operation. By making full use of these world-leading device/circuit technologies, we are exploring future ultra-broadband wireless communication systems as well as spectroscopic/imaging systems for safety and security.

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B3 量子光情報工学 (枝松圭一・小坂英男・三森康義)
Quantum-Optical Information Technology
(K.Edamatsu, H.Kosaka and Y. Mitsumori)

1. 光子を用いた量子情報通信技術の開発

Quantum info-communication technology using photons

半導体や擬位相整合非線形光学結晶を用いた量子もつれ光子の発生・検出方法を開発している。また、導波路媒質における単一光子レベルでの光学非線形性、光子の偏光の量子測定における誤差-擾乱の不確定性関係について研究している。

We investigate the generation and detection technique of entangled photon pairs using semiconductor and quasi-phase-matched (QPM) nonlinear optical materials. Also investigated are the optical nonlinearity of waveguide media at a single-photon level and the error-disturbance uncertainty relation in quantum measurement of photon polarization.

2. 量子中継のための量子メディア変換

Quantum state transfer for quantum repeaters

量子情報通信における通信距離を飛躍的に増大するための量子中継器の実現を目指した光子・電子スピン間の量子メディア変換を行う基礎デバイスの開発を行っている。

We investigate quantum media conversion from a photon to an electron spin for quantum repeaters to extend the transmission distance of quantum info-communication.

3. 半導体量子ドットを用いた量子情報通信

Semiconductor quantum dots for quantum info-communication

量子情報通信への応用を目指した、半導体量子ドットの光物性および量子光学的な性質を研究している。

We investigate the spectroscopic and quantum optical properties of semiconductor quantum dots in view of quantum info-communication technology.

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5.C ナノ分子デバイス基盤技術関連
Nano-Molecular Devices

- C1 ナノ分子デバイス（庭野道夫・木村康男）**
Nano-Molecular Devices (M. Niwano and Y.Kimura)

C1 ナノ分子デバイス (庭野道夫・木村康男) Nano-Molecular Devices(M. Niwano and Y.Kimura)

1. 有機分子デバイスの表面・界面のナノスケール解析・制御
Nanometer-scale analysis and control of surfaces and interfaces of organic molecular devices
有機デバイスの表面・界面での現象をナノスケールで解析し、その動作原理を解明することにより、それらを制御し、有機デバイスの特性を向上させるための研究を行っている。
We have analyzed phenomena on surface and interface of organic devices and have elucidated the principle of their operation on a nanometer scale to improve their performance.
2. 半導体表面用いた生体機能解析
Biodynamic analysis on a semiconductor surface
Si や GaAs 半導体表面上において、細胞やたんぱく質、DNA などの生体物質を赤外吸収法を用いて高感度に検出し、生体機能の解析を行っている。
We have sensitively detected biological materials such as cells, proteins, and DNA molecules on a semiconductor surface such as Si or GaAs and we have analyzed biodynamics
3. 高感度バイオセンシング・システムの研究開発
Research and development of a high-sensitive bio-sensing system
赤外分光法を用いて溶液中で標識を用いずに生体物質を高感度に観測するためのバイオセンシング・システムの開発を行っている。
We have investigated development of a label-free bio-sensing system for high-sensitive detection of biological materials in a solution using infrared absorption spectroscopy.
4. 陽極酸化過程を用いたナノデバイスの開発研究
Research and development of fabricating nanodevices using anodization processes
トップダウンプロセスと陽極酸化過程を組み合わせることによるナノデバイスの作製に関する研究を行っている。
We have investigated fabrication of nanodevices by using both top-down processes and anodization processes.
5. 色素増感太陽電池の開発研究
Research and development of dye-sensitized solar cells
陽極酸化等の電気化学的手法による作製したナノ構造の作製およびその応用を行っている。特に、陽極酸化により作製した TiO₂ ナノチューブの色素増感太陽電池(DSSC)へ応用について研究している。
We have investigated fabrication and application of nanostructures using electrochemical processes such as anodization. Especially, we have applied TiO₂ nanotubes fabricated by anodization to dye-sensitized solar cells (DSSC).

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6. 参考資料

- 6-1 施設のクリーンルームと装置の概要
- 6-2 施設の利用状況（平成 25 年度）
- 6-3 ナノ・スピン工学研究会
国際シンポジウムプログラム

6-1 施設の代表的装置の概要

a-1) ナノ・スピ電子ビーム・リソグラフィ関連

- ナノ・スピ電子描画システム 日本電子 JBX-9300SA
- 用途 ナノスケールのパターン描画
 - 性能 加速電圧: 100 kV
最小線幅: 20 nm
ウェハサイズ: 5mm 角~300mm φ
- ナノ・スピ縮小投影露光システム ニコン NSR-2005i10C
- 用途 縮小投影露光による微細レジストパターンの形成
 - 性能 露光光源: i 線
投影倍率: 1/5
ウェハサイズ: 33mm φ、2 インチ φ
レチクルサイズ: 6インチ角
- マスクアライナー カールズス MJB4
- 用途 集積回路試作用フォトレジストパターンの形成
 - 性能 基板サイズ 5 mm角から最大4 インチ角
マスクサイズ 2 インチ角から5 インチ角
紫外線露光照射度 25mW/cm²
露光分解能 0.8 μm ライン/スペース(バキュームコンタクト時)
- 走査型電子顕微鏡(SEM) 日本電子 JSM7401-FT
- 用途 薄膜表面極微細構造解析
 - 性能 ・2次電子像分解能
加速電圧 15kV : 1.0 nm 保証
1kV GB モード : 1.5 nm 保証
 - ・像種
二次電子像、反射電子像、二次電子+反射電子像、エネルギーフィルタ像
 - ・倍率
LM モード : ×25~19,000、SEM モード : ×100~1,000,000
自動倍率補正機能、倍率瞬時切替機能、像回転補正機能付き
 - ・加速電圧
LM、SEM モード : 0.5~30kV、GB モード : 0.1~4kV
 - ・プローブ電流 10⁻¹³~2×10⁻⁹ A
- マスク電子ビーム描画装置システム 日本電子 JBX-9000ZB(A)
- 用途 電子ビームを用いたマスク作製及びウェーハへの直接描画
 - 性能 最大加速電圧: 50 kV
ビーム電流密度: 10 A/cm²
図形精度: 0.02 μm
最小寸法: 0.1 μm
マスクサイズ: 2.5, 6 インチ φ
ウェハ径: 2, 3 インチ φ, 33mm φ

■ プラズマアッシャー

ヤマト科学

- 用途 試料表面のクリーニングやアッシング
- 性能 プラズマモード: DP/RIE
最大出力: 1000W
ガス: 酸素
カーブトレーサモード可

a-2) 化合物半導体プロセス装置関連

■ 化合物 MBE(VG)

VG V80H

- 用途 化合物半導体薄膜(GaAs/AlAs, InAs/GaSb)のエピタキシャル成長
- 性能 ・ウェハサイズ 2インチ(最大3インチ) 任意形状(In 半田付け)
2インチあるいは2インチウェハの1/4(In Free)
・蒸着源 成膜室1 Ga, In, Al × 2, As × 2, Sb, Si, Be, (Te)
成膜室2 Ga, Al × 2, As, Si

■ SiO₂ 堆積用プラズマCVD装置

日本真空

- 用途 SiO₂の成膜
- 性能 ・到達真空度:
反応室: 3 × 10⁻⁷Torr 以下。
準備室: 2 × 10⁻⁶Torr 以下
・基板加熱 最高 400°C
・反応ガス種 SiH₄、N₂O
・膜厚分布 2インチウェハ内で±4%以下

■ Si₃N₄ 堆積用プラズマCVD装置

日本真空

- 用途 化合物半導体基板への絶縁膜(シリコン窒化膜)の形成
- 性能 ・処理能力
φ2インチ基板 1枚/バッチ
φ33 基板 1枚/バッチ
不定形試料には、基板用ホルダを変えることで対応
・基板加熱温度 反応室 最高 400°C
準備室 最高 300°C
・RF電力 発振周波数 13.56MHz
最高 200W
・導入ガス SiH₄、NH₃、Ar、N₂、O₂

■ 絶縁膜用ドライエッチング装置(RIE)

日本真空

- 用途 化合物半導体ウェハー上のシリコン窒化膜、シリコン酸化膜のエッチング
- 性能 ガス種 CF₄ O₂ H₂
到達圧力 1.3 × 10⁻⁵Pa 以下。
プロセス圧力 0.67~13.3Pa 以下
基板冷却機構 -30°C~25°C
基板処理枚数 φ2 インチ基板×1 枚/バッチ

- 多目的電子ビーム蒸着装置 (n型蒸着器) 日本真空
- 用途 化合物半導体にp型及びn型電極材料を電子ビーム・抵抗加熱で蒸着し、熱処理を行う。
 - 性能
 - ・n型金属蒸着用電子ビーム蒸着装置
 - ウェハサイズ 不定形(最大2インチ)
 - 電子ビーム蒸着源数 1
 - 抵抗加熱蒸着源数 2
 - ・p型金属蒸着装置
 - ウェハサイズ 不定形(最大2インチ)
 - 蒸着源数 3
 - ・n/p型用赤外線熱処理装置
 - ウェハサイズ 不定形(最大2インチ)
 - 加熱温度 900°C±5°C以内
 - 雰囲気ガス 窒素、アルゴン、水素

- イオン発生システム (ECR-RIE) 日本真空
- 用途 Cl_2 ガスを用いた化合物半導体等の異方性エッチング
 - 性能
 - ・チャンバ構成 エッチング室、ロードロック室
 - ・到達真空度 10^{-8} Torr 台
 - ・動作圧力 $10^{-3} \sim 10^{-5}$ Torr
 - ・基板サイズ 2インチ、及び不定形
 - ・エッチング速度 $\sim 2000 \text{ \AA}/\text{min}$ (GaAs)
 - ・エッチング分布 2インチ基板内±10%以下
 - ・基板冷却 基板ホルダー水冷式
 - ・イオン源 ECRパケット型
 - ・イオンエネルギー 200~1000V
 - ・プロセスガス Cl_2 、Ar、 O_2

- 半導体パラメータアナライザ ソニーテクトロニクス
- 用途 半導体電子デバイス等の電気的特性の評価
 - 性能
 - ・ソースモニタユニット数 6
 - ・分解能 8mV 4fA
 - ・最大電圧・電流 200V 100 mA
 - ・カーブトレーサモード可

- 高機能マイクロカー測定装置装置 ネオアーク
- 用途 約 $2 \mu\text{m}$ の領域における面内および極 kerr 効果の測定
 - 性能
 - 光源: 半導体レーザー 波長 650 nm
 - レーザースポットサイズ: 約 $2 \mu\text{m}$ φ
 - 最大印加磁場: 1.0T
 - 温度: 1.5K-400K(室温以下は液体 He 使用)
 - ステージ空間分解能 $1 \mu\text{m}$

- 多機能薄膜材料評価 X線回折装置 (2次元検出器付 XRD) Bruker
- 用途 強力 X線源と2次元検出器を用いた高速な X線回折測定
 - 性能
 - X線源: Cu
 - 検出器: シンチレーション検出器、2次元検出器
 - 試料ステージ: 5軸試料ステージ

■ 接触段差計 (Dektak150)

アルバック

- 用途 試料方面に形成された段差、上面形状、表面粗さなどの評価
- 性能 試料サイズ: 150 mm φ 以下
高さ方向分解能: 0.1nm (@6.55 μm range)

■ マイクロプローブ式低温ホール効果測定装置

理工貿易

- 用途 半導体材料・ヘテロ構造におけるキャリア移動度・キャリア密度の評価
- 性能 測定方法: Van der Pauw 法
ステージ温度範囲: 20K~400K
最大印加磁界強度: 0.4T

a-3) シリコンプロセス装置関係

■ ナノヘテロ界面処理加工システム

- 用途 Si-Ge-C 系半導体ナノヘテロ構造形成及びその界面処理などの加工を行う。
- 性能 Si-Ge 系薄膜のエピタキシャル成長や不純物ドーピングが可能。
300-1100°C での各種ガス雰囲気中での熱処理が可能。

■ ナノヘテロ分析システム

- 用途 Si-Ge-C 系半導体ナノヘテロ構造の高精度分析を行う。
- 性能 Si-Ge-C 系半導体ナノヘテロ構造の原子結合・歪状態(レーザラマン分光システム)、薄膜積層構造(分光エリプソメータ)、電気抵抗(4探針法抵抗率測定器)の評価分析が可能。

■ 半導体電気磁気複合特性測定システム

HP 他組上システム

- 用途 直流ホール効果測定用
- 性能 磁場強度 6.9kOe(ギャップ 60mm 時)。クライオスタットにより試料台温度を 10K まで冷却可能。

■ 常圧 CVD 装置

- 用途 熱 CVD 法により SiO₂、PSG、BSG の薄膜形成を行う。
- 性能 200~400°Cでの熱 CVD 法 により、SiO₂、PSG、BSG を形成可能 (2チャンバー)。
バッチ内膜厚分布±5%以内。

■ 原子スケール評価分析システム (AFM/STM)

オミクロン

- 用途 半導体プロセスの原子スケール評価分析等用。
- 性能 超高真空 STM、コンタクトモード AFM、ノンコンタクトモード AFM。
LEED、オージェ、XPS 等可能。試料通電加熱可能。
装置接続延長管付

■ Si系RIE

- 用途 シリコン加工用ドライエッチング装置(アネルバ EMR510 特)
Si 基板上的 Si 系半導体のエッチングを行う。
SiO₂加工用ドライエッチング装置(アネルバ DEM-451 特)
Si 基板上的 Si および SiO₂のエッチングを行う。
メタル加工用ドライエッチング装置(アネルバ L-451DA-L)
Si 基板上的金属のエッチングを行う。
- 性能 シリコン加工用ドライエッチング装置
Si 基板上的 Si 系半導体のエッチングが可能(ECR型)。最大6インチウエハ。試料皿にのる不定形ウエハ可能。補助磁場印加、RFバイアス印可可能。
導入ガス: Cl₂、SiCl₄、BCl₃、SF₆、O₂、H₂、N₂、Ar
SiO₂加工用ドライエッチング装置
Si および Si 基板上的 SiO₂のエッチングが可能 (RF 励起平行平板型)。
導入ガス: CF₄、SF₆、O₂、H₂、N₂、Ar
メタル加工用ドライエッチング装置
Si 基板上的金属のエッチングが可能 (RF 励起平行平板型)。
エッチング室用ガス: N₂、Ar、H₂、BCl₃、SiCl₄、Cl₂、CF₄、SF₆、O₂
アッシング室用ガス: O₂、N₂

■ X線光電子分光装置 (ESCA)

SSI SSX-100、Kratos
AXIS-NOVA

- 用途 SSI SSX-100 表面元素分析用
Kratos AXIS-NOVA 表面元素分析用、表面元素分布イメージング用
- 性能 SSI SSX-100
単色化 X 線源(ALK α)
最少分析領域 150 μm
最少パスエネルギー 25eV
最高エネルギー分解能 0.58eV (Ag 3d 5/2)
Kratos AXIS-NOVA
単色化 X 線源(AIK α)
スペクトルモード: 最少分析領域 15 μm
最少パスエネルギー 5eV
最高エネルギー分解能 0.48eV (Ag 3d 5/2)
イメージングモード: 最高空間能 3 μm

■ ワイドレンジナノ形状測定システム

島津製作所 FT-3500

- 用途 表面ナノ形状測定用
- 性能 レーザー顕微鏡部
408nm 紫外半導体レーザスキャン方式
最大光学ズーム倍率 6 倍
観察視野 21~560 μm 最高ピクセル分解能 21nm
プローブ顕微鏡部
AFM(コンタクト、ダイナミック、位相)モード
表面電位モード
電流モード
磁気力モード
最大走査範囲(水) 30 μm × 30 μm × (高さ) 4 μm
最高制御分解能(水平) 0.45 μm × (高さ) 0.06 μm

a-4) 配線プロセス関係

- ナノ・スピンメタルスパッタリングシステム アネルバ EVP-38877
- 用途 半導体集積プロセスにおける配線用 Al/Ti 薄膜の成膜
 - 性能 ターゲット材 Al-Si(1%)、Ti
基板ホルダ 33ミリφ、2インチφ、4cm角以下のカットウエハ等
処理枚数 33ミリφウエハ 25枚/ロット
膜厚分布 φ200ミリ内±5%以内
到達真空度 3×10^{-6} Pa(スパッタ室)
- アナライザ アジレント HP-4156C
- 用途 トランジスタの電圧-電流特性等各種電子デバイスの電気特性の測定
 - 性能 高分解能電圧電流ソース・モニタ・ユニット(1fA/2μV-100mA/100V) × 4
電圧測定ユニット × 2
電圧源ユニット × 2
- ボンダー ウェストボンド 7476D
- 用途 集積化チップとパッケージ間の信号線配線
 - 性能 ワイヤー Al、Au
最大倍率 60倍の可変ズーム顕微鏡
始点・終点の超音波出力/発生時間の独立設定が可能
パッケージの加熱可能
- マスクアライナー カールズス MJB4
- 用途 集積回路試作用フォトリソパターン形成
 - 性能 基板サイズ 5mm角から最大4インチ角
マスクサイズ 2インチ角から5インチ角
紫外線露光照度 25mW/cm²
露光分解能 0.8μmライン/スペース(バキュームコンタクト時)
- スパッタ装置 アネルバ
- 用途 高密度金属配線形成、金属電極形成、シリサイド用高融点金属薄膜形成
 - 性能 φ4"カソード × 3基
最大搬送基板サイズ: φ4"
基板加熱: MAX350°C
到達真空度: 3×10^{-6} Pa 以下
- 熱処理炉 東京エレクトロン
- 用途 ゲート酸化膜、フィールド酸化膜の形成、SiO₂、PSGなどの熱処理、イオン注入後の熱処理、シンタリング、アロイング
 - 性能 O₂、N₂、Ar、H₂、H₂+O₂雰囲気中での熱処理が可能。
ヒータ加熱方式
600°C~1050°C: 4体
200°C~800°C: 2体

■ 金属蒸着装置

日本シード研究所 M95-0019

- 用途 金属薄膜(アルミニウム)の蒸着(抵抗加熱型)
- 性能 蒸着源ポート数:2
対応ウェハサイズ:33mmφ、2”、6”、8”
膜厚コントローラによる蒸着レートの制御が可能
基板回転機構付き

■ LSI テスタ

HP9494

- 用途 アナログ及びデジタル LSI チップの動作測定・検証
- 性能 HP9494A ミックスドシングル LSI テストシステム
30MHz 12Bit 任意波形発生器
1MHz 16Bit デジタイザ

■ CAD システム

セイコー電子 SX-9000

- 用途 集積回路パターン作製用 CAD
- 性能 ・SX9000 による CAD パターン作製
・JEOL52 フォーマットへの CAD データコンバート機能

■ 表面ナノ加工装置

日本ビーコ

- 用途 走査型プローブ顕微鏡の探針によるナノメートルスケールの加工機能及びマニピュレーション及び走査型プローブ顕微鏡による各種プローブ顕微鏡像の観察
- 性能 最大試料サイズ:210 mm
ステージ可動範囲:180 mm×150 mm
最大走査範囲:XY:80 μm、Z:9 μm
ナノマニピュレーション機能:スクラッチ、陽極酸化
プローブ顕微鏡:STM、AFM(コンタクト、タッピング、摩擦力、電流)、FEM、SPoM、SCM、MFM

■ 非接触段差・粗さ計測装置

レーザーテック

- 用途 試料表面のマイクロメートルからナノメートルスケールの段差や粗さを非接触にて測定する
- 性能 ピクセル数:2048×2048
階調:16ビット
共焦点顕微鏡機能による高さ測定機能(測定精度(σ):0.02 μm)
ミラウ型干渉による微細形状計測機能(測定精度(σ):0.0007 μm)

■ イオンビーム加工解析装置(FIB-SEM)

SII-NT NVision40(A)

- 用途 集束イオンビームによる微細加工と SEM 観察
- 性能 Ga イオンビーム最大加速電圧:30kV
電子ビーム最大加速電圧:30kV
Ar イオンビーム最大加速電圧:1kV
堆積可能膜:Pt,C,SiO₂
二次イオン質量分析器(SIMS)
エネルギー分散型 X 線分光器(EDS)
走査透過型電子顕微鏡(STEM)機能
TEM 試料作製用マニピュレータ
最大サンプルサイズ:3 インチφ

■ レーザー直接描画装置

ネオアーク

- 用途 フォトリソグラフィ用レジストに直接描画する
- 性能 レーザー光源波長:375nm
最少描画線幅:1 μ m
最大描画範囲:50 mm × 50 mm

■ ナノ立体加工装置(AFM)

エスアイアイ・ナノテクノ
ロジー(株) SPA400

- 用途 原子間力顕微鏡像の取得及びその解析
- 性能 AFM(コンタクトモード)、DFM(ノンコンタクトモード・サイクリックコンタクトモード・フェーズモード)
最大試料サイズ:35 mm ϕ
走査範囲:20 μ m、150 μ m

6-2 施設の利用状況(平成25年度)

平成25年度 ナノ・スピンの実験施設 利用登録状況

(平成26年3月31日まで)

ナノ・スピン実験施設

	A/B	研究室名	利用責任者		人数
常駐 研究室	A	大野研	池田正二	大野教授 松倉教授 池田准教授 山ノ内助教 (研究員) 陳 イビオエライ 中山 (D3)水沼 (D2)石原 金井 (M2)張 石川 (M1)岡田 都澤 竹内 久保田 堀川 (B4)平山 シャンハン 伊藤 渡部 (研究留学生)林 張 Samic 楊 (CSIS) 佐藤助教 深見助教 (技術専門職)五十嵐 村畑 (共同研究員)大嶋 笠井教授 木下教授 山本 (ホスト)ソフィー (研究支援員)平田 岩沼 川戸 後藤	38
	A	庭野研	木村康男	庭野教授 木村准教授 (D3)小島 ハトウ 桜井 (D2)但木 (M2)飯野 戸邊 山下 茂住 (M1)齋藤 中山 高沖 松村 (B4)山田 千田 (研究員)室田(*B 登録) 南(*B 登録)	23
			平野愛弓	平野准教授 (M1)石成 (B4)小林 鈴木 (M2)Matteo	
	A	佐藤研	櫻庭政夫	佐藤教授 櫻庭准教授 秋間助教 (M2)上野 (M1)李 刑部 (B4)岩橋 茂木	8
	A	中島研	小野美 武	中島教授 小野美助教 (M2)辻 山田	4
	A	尾辻研	末光哲也	尾辻教授 末光准教授 佐藤助教 鷹林助教 (D2)吉田 (M2)小林 栗田 江藤 矢部 (M1)畠山 川崎 フシン (B4)菅原	13
	B	枝松研	三森康義	枝松教授 小坂准教授 三森准教授	3
	A	共通部	岩見友里香	岩見技術職員 森田技術職員	2
非常駐 研究室	B	新田研	好田 誠	好田准教授 (D3)塩貝 Tim (D1)長澤 大杉 (M2)大橋 佐々木 栗田 (M1)柳 小又 小野 岡安 (B4)高砂 辻 中村 吉住 青木 王	18
	B	安藤研	大兼幹彦	大兼准教授 永沼助教 (研究員) Mohammed KHAN (D3)河田 (D1)向山 (M2)斉 猿山 森廣 (M1)栗本	9
	B	遠藤研	遠藤哲郎	遠藤教授 (研究支援員)東	2
	B	島津研	島津武仁	島津教授 (M2)堀田	2
	B	羽生研	夏井雅典	羽生教授 夏井助教 望月助教 (研究員)高子 (支援員)玉越 (研究支援者)鈴木 松永	7
	B	鷲尾研	川島和之	川島助教 (M1)千葉 畠山 伊藤 森	5
	B	田中研	田中 徹	田中教授 木野助教 (D1)谷	3
	B	末光研	吹留博一	吹留准教授 (D3)朴 (M2)船窪 (PD)焦 (共同研究員)館野 (M1)須藤 (B4)田島	7
	B	高桑研	小川修一	小川助教 (D1)尾白 唐 (M2)阿加 (M1)林 西本 (留学)Radec	7
	マスクのみ	北上研	菊池伸明	菊池助教 (D2)古田 (M1)草薙	3

合計

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共通利用対応装置 利用時間

装置名 (※は共通利用対応装置、Pはパソコン対応)	平成25年3月1日から平成26年2月28日まで												
	M/P	M/P	M/P	M/P	M/P	M/P	M/P	M/P	M/P	M/P	M/P	M/P	合計
佐藤・櫻庭研													4461
大野研													2389 2093
屋野・木村研													586 2312
池田産科研													1321 578
中島研													2863 6113
枝松・小坂・三森研													21 20
尾辻・永光研													307 563
徳島共通部													503 502.75
羽生研													182
永光研													74 288
野原・小谷研(固体電子研究室)													
金子研													
安藤研(応用物理学専攻)													
新田研(知能電子材料専攻)													28 268
北上研(多相物質科学研究所)													6 30
石原研(理学研究科)													3
田中研(バイオロボティクス専攻)													
栗川研(流体力学研究所)													
高橋研													
佐藤研													
森田研													
計	842	148	579	53	212	31	708	345					105
													9093 15174
													24267

6-3 ナノ・スピ工学研究会

21世紀に求められる高度な情報通信の実現には、ナノテクノロジーに基づく材料デバイス技術からシステム構築までの総合科学が必要である。「ナノ・スピン実験施設」は、この情報通信を支える総合科学技術の中の、ナノテクノロジーに基づいた電子の電荷・スピンを駆使する基盤的材料デバイス技術の研究を総合的・集中的に推進することを目的に、本研究所附属研究施設として平成16年4月1日に設置された。本研究会は、この施設を中心に展開して得られた成果にもとづき、広くナノエレクトロニクス・スピントロニクスに関連した科学技術に関して十分議論することを目的としている。平成25年度は以下のように計4回の講演会を実施した。

- 第67回 平成25年5月17日 14:00 - 15:30
"Nanomaterials with charged quantum dots for solar energy conversion and advanced sensing applications"
Vladimir Mitin (Dept. Electrical Eng., University at Buffalo, USA)
- 第68回 平成25年6月27日 15:00 - 16:00
"On the Possible Generalization of TFT and ULSIC Technologies"
Yue Kuo (Texas A&M University, USA)
- 第69回 平成25年12月16日 12:35 - 18:15, 12月17日 09:30 - 15:25
「60GHz帯CMOS無線機の研究開発」
○岡田健一（東工大）
「超100GHz帯増幅器技術の進展とその通信への応用」
○佐藤 優・川野陽一・芝 祥一・松村宏志・高橋 剛・鈴木俊秀・中舎安宏・原 直紀・高橋 剛（富士通）
「共鳴トンネルダイオードを用いたテラヘルツ発振器における最近の進捗と今後の展望」
○鈴木左文・浅田雅洋（東工大）
他8件
- 第70回 平成26年1月31日 15:00 - 16:00
"Digital implementation of multimodel bioinspired spiking neural networks based on SIMD and AER"
Jordi Madrenas (Technical University of Catalunya, Spain)

「LiNbO₃導波路を用いたチェレンコフ位相整合型テラヘルツ光源開発とその応用」
塩田和教・小野 茂・入澤昭好・今村元規（アドバンテスト）他10件

The 2nd RIEC International Symposium on Brain Functions and Brain Computer

Date: February 21 - 22, 2014

Place: Laboratory for Nanoelectronics and Spintronics,
Research Institute of Electrical Communication,
Tohoku University

Organizers:

Symposium Co-Chairs

Koji Nakajima, Tohoku Univ., Michio Niwano, Tohoku Univ.

Program Committee

Takahiro Hanyuu, Tohoku Univ., Shigeo Sato, Tohoku Univ., Ayumi Hirano, Tohoku Univ.

Organizing Committee

Shigeo Sato, Tohoku Univ., Ayumi Hirano, Tohoku Univ., Junichi Murota, Tohoku Univ.

Sponsored by

Laboratory for Nanoelectronics and Spintronics,
Research Institute of Electrical Communication (RIEC), Tohoku University

Program

February 21 (Friday)

13:30 - 13:35 Opening Remarks

M. Niwano, Tohoku Univ., Japan

--- Session 1 (Chair: N. Katayama) ---

13:35 - 14:05 [S1-1] Nanoelectronic devices and materials for neuroscience

A. Offenhäuser, Jülich research center, Germany

14:05 - 14:25 [S1-2] Intrinsic activity of defined neuronal networks in culture

H. Yamamoto, T. Tanii, Waseda Univ., Japan

14:25 - 14:45 [S1-3] The number of astrocytes is critical for the synaptic transmission in a single cultured neuron

S. Katsurabayashi, T. Hoshiyama, S. Iwamoto, Y. Aonuma, N. Kubo, M. Kubo, K. Takasaki, K. Kubota, K. Mishima, M. Fujiwara, M. Niwano, K. Iwasaki, Fukuoka Univ., Japan

14:45 - 15:00 Break (15 min)

--- Session 2 (Chair: N. Onizawa) ---

15:00 - 15:20 [S2-1] A role of activity-dependent competition in regulating the critical period of cortical plasticity

S. Kubota, I. Sakurai, M. Niwano, Yamagata Univ., Japan

15:20 - 15:40 [S2-2] Chaotic Boltzmann machines: Deterministic implementation of stochastic neural networks

H. Suzuki, Univ. of Tokyo, Japan

15:40 - 16:10 [S2-3] Resilient and energy efficient memories based on neuro-inspired codes

V. Gripon, Télécom Bretagne, France

16:10 - 16:25 Break (15 min)

16:25 - 17:25

--- Poster Session (@ Room A405) ---

[P-1] Live-cell, label-free identification of excitatory-inhibitory neurons in culture using surface micropatterns

S. Kono, H. Yamamoto, T. Kushida, T. Tanii, Waseda Univ., Japan

[P-2] Computational analysis of selective recruitment for subcortical nerve fibers by smooth biphasic waveforms

A. Ueno, N. Katayama, A. Karashima, M. Nakao, Tohoku Univ., Japan

[P-3] Axon guidance of cultured hippocampal neurons based on microcontact printing

H. Takaoki, S. Osanai, A. Hirano-Iwata, Y. Kimura, M. Niwano, Tohoku Univ., Japan

[P-4] A current-sampling method for simultaneous L-glutamate and field potential recordings in mouse hippocampal slices

R. Matsumura, R. Tezuka, A. Hirano-Iwata, M. Niwano, M. Sugawara, T. V. P. Bliss, Tohoku Univ., Japan

[P-5] Reconstitution and characterization of human ether-a-go-go-related gene channels in microfabricated silicon chips

Y. Ishinari, A. Hirano-Iwata, Y. Kimura, M. Niwano, Tohoku Univ., Japan

[P-6] Period tuning of the optokinetic response: A new cerebellar dependent predictive motor learning?

S. Miki, B. Robert, Y. Hirata, Chubu Univ., Japan

[P-7] An integrated circuit system for multi-channel current stimulations on neuronal circuits

S. Kameda, Y. Hayashida, D. Akita, Y. Tanaka, T. Yagi, Osaka Univ., Japan

[P-8] A system for solving optimization problems using the inverse function delayless neuron model

Y. Watanabe, Y. Hayakawa, S. Sato, K. Nakajima, Toho Univ., Japan

[P-9] Optimal scheduling of a disruption tolerant network using a neural network

D. Sasaki, Y. Hayakawa, S. Sato, K. Nakajima, Toho Univ., Japan

18:30 - 20:30 Banquet

February 22 (Saturday)

--- Session 3 (Chair: S. Kubota) ---

- 9:00 - 9:20 [S3-1] Hippocampal EEG activity of freely behaving mouse in the virtual environment
N. Katayama, K. Hidaka, T. Araya, A. Karashima, M. Nakao, Tohoku Univ., Japan
- 9:20 - 9:40 [S3-2] Network mechanisms of high-frequency oscillation in the hippocampus
H. Kamiya, Hokkaido Univ., Japan
- 9:40 - 10:00 [S3-3] Microfabricated silicon chips for recording hERG channel activities
A. Hirano-Iwata, Y. Ishinari, Y. Kimura, M. Niwano, Tohoku Univ., Japan
- 10:00 - 10:15 Break (15 min)

--- Session 4 (Chair: S. Katsurabayashi) ---

- 10:15 - 10:35 [S4-1] Simulation of oculomotor control and learning by a large scale spiking cerebellar neuronal network model
K. Inagaki, Y. Hirata, Chubu Univ., Japan
- 10:35 - 10:55 [S4-2] Dynamical simulation of retinal ganglion cell responses to natural scenes
T. Yagi, T. Sanada, H. Okuno, R. Ishida, J. Hasegawa, N. Nakashima, Osaka Univ., Japan
- 10:55 - 11:10 Break (15 min)

--- Session 5 (Chair: K. Nakajima) ---

- 11:10 - 11:40 [S5-1] Holistic pattern processing in large-scale cortical network models
P. Herman, Royal Institute of Technology, Sweden
- 11:40 - 12:00 [S5-2] Silicon neuron using stochastic logic
S. Sato, K. Nakajima, H. Akima, M. Sakuraba, Tohoku Univ., Japan
- 12:00 - 12:05 Closing Remarks
K. Nakajima, Tohoku Univ., Japan

Program

March 5 (Wednesday)

Room: 4F, Conference Room, *Laboratory for Nanoelectronics and Spintronics*

- 13:30 ~ 13:35 Opening Remarks
 Michio Niwano (Tohoku University, Japan)
(Chair: Toshio Ogino)
- 13:35 ~ 14:20 Self-organized TiO₂ nanotube arrays: Latest features and applications
 Patrik Schmuki (University of Erlangen Nuremberg, Germany)
- 14:20 ~ 14:40 Formation of an anodic titanium oxide nanotube film and its application to a
 front-illuminated dye-sensitized solar cell
 Ryota Kojima, Teng Ma, Yasuo Kimura, and Michio Niwano (Tohoku University,
 Japan)
- 14:40 ~ 15:00 Fabrication of highly ordered structure in TiO₂ nanotubes for hybrid solar cells
 Teng Ma, Ryota Kojima, Daisuke Tadaki, Yasuo Kimura, and Michio Niwano
 (Tohoku University, Japan)
- 15:00 ~ 15:20 Metal-supported titanium oxide nanotube micro gas sensors
 Yasuo Kimura (Tohoku University, Japan)
- 15:20 ~ 15:30 Coffee break
- (Chair: Yasuo Kimura)
- 15:30 ~ 16:00 Functional Applications of Nanostructured Silicon
 Nobuyoshi Koshida (Tokyo University of Agriculture and Technology, Japan)
- 16:00 ~ 16:30 Reliable Metaldeposition into TiO₂ Nanotubes and Use in Interdigitated Electrode
 Structures
 Ning Liu, and Patrik Schmuki (University of Erlangen-Nuremberg, Germany)
- 16:30 ~ 16:50 Growth and functionalization of low-dimensional nanocarbons by advanced
 plasma processing
 Toshiaki Kato, Bin Xu, Hiroo Suzuki, Toshiro Kaneko (Tohoku University, Japan)
- 16:50 ~ 17:10 Self-Shaping Deposition of Monolayer Graphene Oxide Flakes on Chemically
 Modified Surfaces
 Toshio Ogino and Toshiyuki Takami (Yokohama National University, Japan)

March 6 (Thursday)

Room: 4F, Conference Room, *Laboratory for Nanoelectronics and Spintronics*

(Chair: Yasuo Kimura)

- 9:00 ~ 9:30 Interaction of spin polarized electrons with adsorbed chiral molecules: reactions and transport
Richard Rosenberg (Advanced Photon Source, Argonne National Laboratory, Argonne, IL, USA)
- 9:30 ~ 9:50 Excitation of light emission by electron tunneling in the THz spectral range
T. Tsunoda, S. Katano, and Y. Uehara (Tohoku University, Japan)
- 9:50 ~ 10:10 Tunable preparation of graphene oxide and its metal nanoparticle composites
Yuta Nishina^{1,2} (¹RCIS Okayama University, ²JST PRESTO, Japan)
- 10:10 ~ 10:30 Room temperature atomic layer deposition of oxide films on flexible materials
F. Hirose, K. Kanomata, S. Kubota, and K. Hirahara (Yamagata University, Japan)
- 10:30 ~ 10:40 Coffee break

(Chair: Ko-ichiro Miyamoto)

- 10:40 ~ 11:10 Tools for chip-based recording and stimulation of neuronal network activity
Philipp Rinklin¹, Martin Hüske¹, Alexey Yakushenko¹, Cony Herrera¹, Kay Krause¹, Anna Czeschik¹, Enno Kästelhön¹, Andreas Offenhäusser^{1,2}, and Bernhard Wolfrum^{1,2} (¹Institute of Bioelectronics (PGI-8/ICS-8) and JARA—Fundamentals of Future Information Technology, Forschungszentrum Jülich, Germany, ²IV. Institute of Physics, RWTH Aachen University, Germany)
- 11:10 ~ 11:30 Microfabricated Si chips with integrated hERG channels as a platform for drug safety screenings
Ayumi Hirano-Iwata¹, Yutaka Ishinari¹, Yasuo Kimura², and Michio Niwano^{1,2} (¹Graduate School of Biomedical Engineering, Tohoku University, ²RIEC, Tohoku University, Japan)
- 11:30 ~ 13:00 Lunch
- 13:00 ~ 14:20 Poster session

(Chair: Shinichiro Umemura)

- 14:30 ~ 15:00 On the Science of Sonoporation: Biophysical Dynamics and Subcellular Impact
Alfred Cheuk Hang Yu (University Hong Kong, China)
- 15:00 ~ 15:30 Roles of High-Speed Imaging in Ultrasound-Guided HIFU Treatment
S. Umemura, S. Sasaki, H. Sasaki, R. Takagi, K. Matura, and S. Yoshizawa (Tohoku University, Japan)

- 15:30 ~ 16:00 High Frame Rate Ultrasonography for Measurement of Tissue Dynamics
Hideyuki Hasegawa and Hiroshi Kanai (Tohoku University, Japan)
- 16:00 ~ 16:15 Coffee break
- (Chair: Ko-ichiro Miyamoto)
- 16:15 ~ 16:45 Fabrication of fiber optic biosensor based on surface-enhanced Raman scattering
Takashi Katagiri¹, Masahiro Nagaoka², Yuji Matsuura^{1,2} (¹School of Engineering and ²Graduate School of Biomedical Engineering, Tohoku University, Japan)
- 16:45 ~ 17:15 Osteoblast and macrophage response to hydroxyapatite and alpha-type alumina adsorbed with bovine serum albumin
M. Kawashita¹, J. Hayashi¹, T. Kudo² and H. Kanetaka² (¹Graduate School of Biomedical Engineering and ²Graduate School of Dentistry, Tohoku University, Japan)
- 17:15 ~ 17:45 Recent developments and applications of the chemical imaging sensor systems
Tatsuo Yoshinobu (Tohoku University, Japan)

March 7 (Friday)

Room: 4F, Conference Room, Laboratory for Nanoelectronics and Spintronics

(Chair: Toshiro Kaneko)

- 9:00 ~ 9:30 Development of Medical Plasma Equipment for Blood Coagulation as a Novel Minimally Invasive Treatment
Hajime Sakakita¹, Yuzuru Ikehara¹, Nobuyuki Shimizu², Satoru Kiyama¹, Sanae Ikehara¹, Jaeho Kim¹, Hayao Nakanishi³ (¹National Institute of Advanced Industrial Science and Technology (AIST), Japan, ²SANNO Hospital, International University of Health and Welfare, Japan, ³Aichi Cancer Center Research Institute, Japan)
- 9:30 ~ 10:00 Control of Cell Membrane Transport Using Atmospheric Pressure Non-Equilibrium Plasma
Toshiro Kaneko¹, Shota Sasaki¹, and Makoto Kanzaki² (¹Department of Electronic Engineering and ²Department of Biomedical Engineering, Tohoku University, Japan)
- 10:00 ~ 10:15 Coffee break

(Chair: Masashi Sahashi)

- 10:15 ~ 10:45 Multifunctional Fe₃O₄-ZnO Core-Shell Nanocrystals for Dendritic Cell-based Cancer Immunotherapy
Young Keun Kim (Korea University, Korea)
- 10:45 ~ 11:15 Study of Biological Applications using Nanostructures: AFM based Nano-Bio Applications
Yu Jin Kim (Korea University, Korea)
- 11:15 ~ 11:45 Structure and dynamics on submicron scales in Phosphatidylinositol-Containing Supported Lipid Bilayers
Ryugo Tero¹, Toshinori Motegi¹, Kingo Takiguchi², Yohko Tanaka-Takiguchi² (¹ Toyohashi University of Technology, Japan, ²Nagoya Univ., Japan)
- 11:45 ~ 13:15 Lunch

(Chair: Masashi Sahashi)

- 13: 15 ~ 13:45 Voltage Controlled Magnetization Switching with Magnetoelectric (ME) Effect in Sesquioxide
Tatsuo Shibata¹, Takuya Ashida², Michihiro Oida², Naoki Shimomura², Tomohiro Nozaki², Atsuo Ochi¹, and Masashi Sahashi² (¹Advanced Technology Development Center, TDK Corporation, Japan, ²Tohoku University, Japan)

- 13:45 ~ 14:15 Anisotropic magnetoresistance of Co/Pt nano-contacts
Muftah Al-Mahdawi, Yohei Shiokawa, and Masashi Sahashi (Tohoku University, Japan)
- 14:15 ~ 14:45 Study on Tunneling Magneto-Resistance in Hexagonal $Mg_xZn_{1-x}O$ based MTJs Prepared by Molecular Beam Epitaxy
M. Belmoubarik¹, H. Sato¹, T. Nozaki¹, H. Endo², and M. Sahashi¹ (¹Tohoku University, ²Iwate Industrial Research Institute, Japan)
- 14:45 Closing